

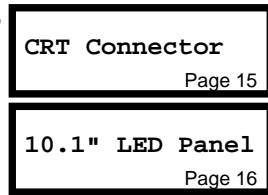
Miata2.0 (NM3) Block Diagram

01

PCB STACK UP 6L

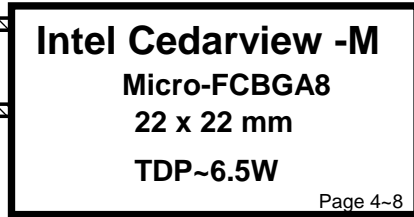
LAYER 1 : TOP
LAYER 2 : SGND
LAYER 3 : IN1
LAYER 4 : IN2
LAYER 5 : VCC
LAYER 6 : BOT

SYSTEM POWER +3VS5/+5VS5(RT8223)	PAGE 27
SYSTEM CHARGER (OZ8681LN)	PAGE 28
Dis-charge (G5934RZ1U) +3VSUS/+3V/+5V/+1.5V/+3VLAVCC	PAGE 29
+1.05V(G5173)	PAGE 30
+1.8V (G9661)	PAGE 31
DDR 3 +0.75V_DDR_VTT/+1.5VSUS(RT8207)	PAGE 32
CPU CORE (RT8152FGQW)	PAGE 33

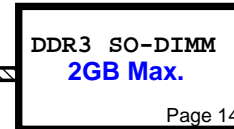


CRT

LVDS



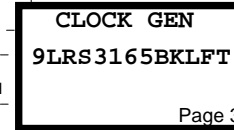
DDR3 1066MT/s (Single Channel)



32.768KHz

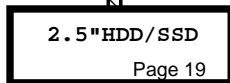
DMI x2

CLK_PCI_DMI,CLK_PCI_DMI#
HCLK_CPU,HCLK_CPU#; HCLK_MCH,HCLK_MCH#
DREFSSCLK,DREFSSCLK#; DREFCLK,DREFCLK#
CLK_PCIE_ICH,CLK_PCIE_ICH#,CLK_USB_48,CLK14M_ICH
CLK_PCIE_SATA,CLK_PCIE_SATA#,PCLK_ICH

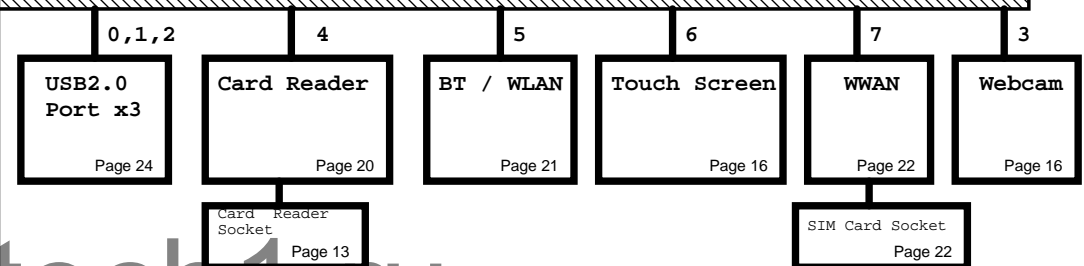


1.4,3.18MHz

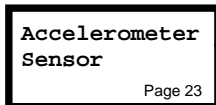
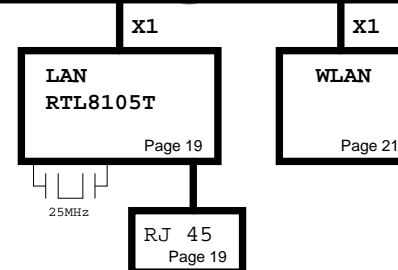
SATA



USB 2.0

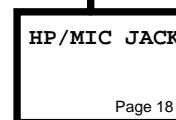
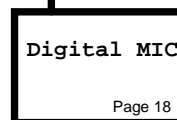
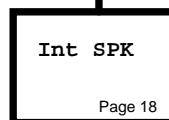
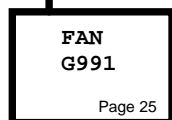
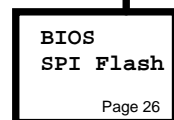
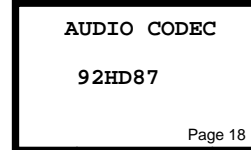
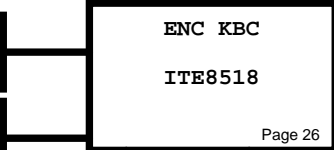
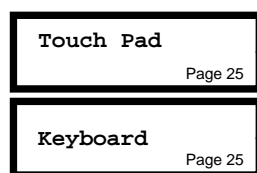


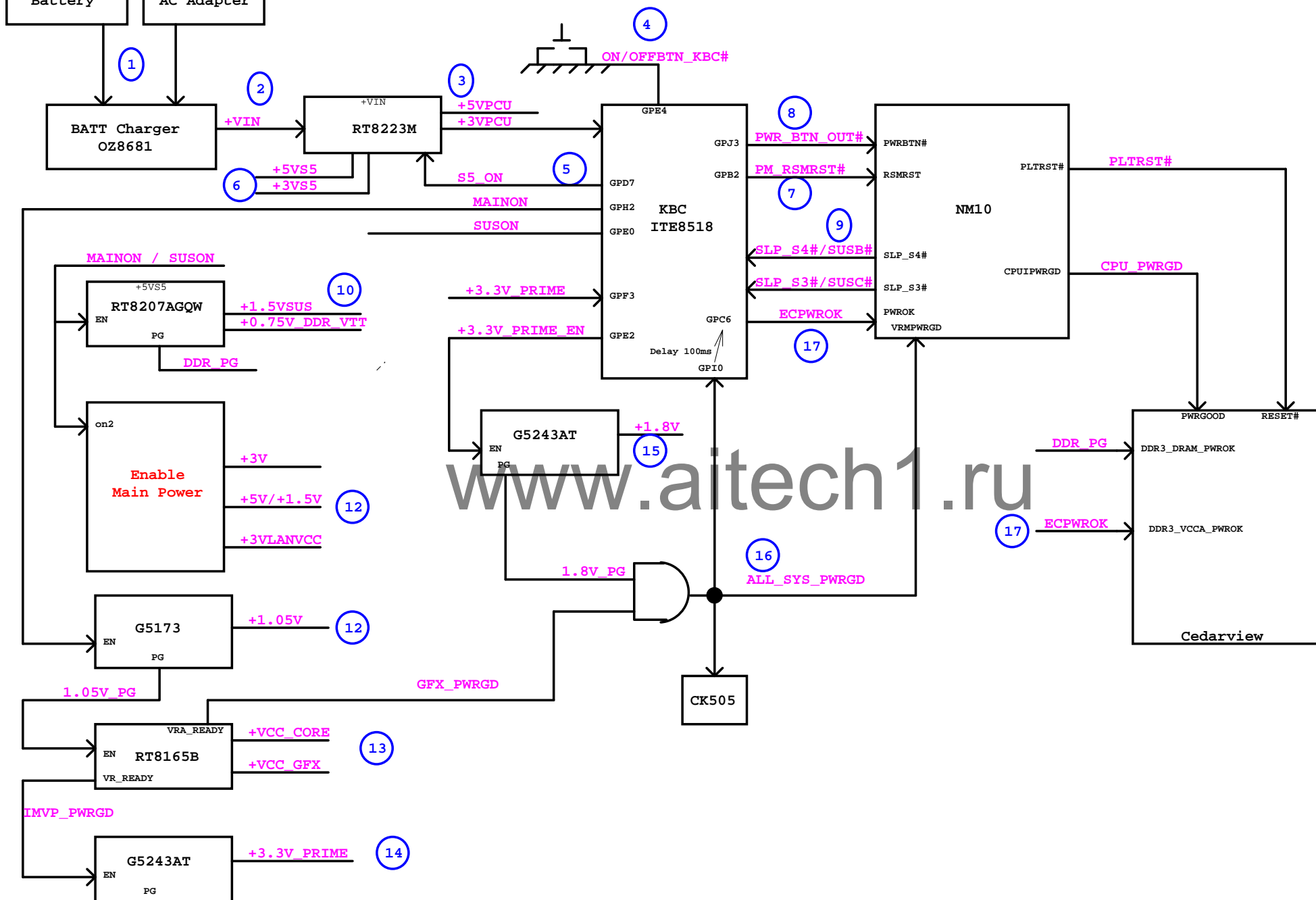
PCI-Express



LPC

HDA



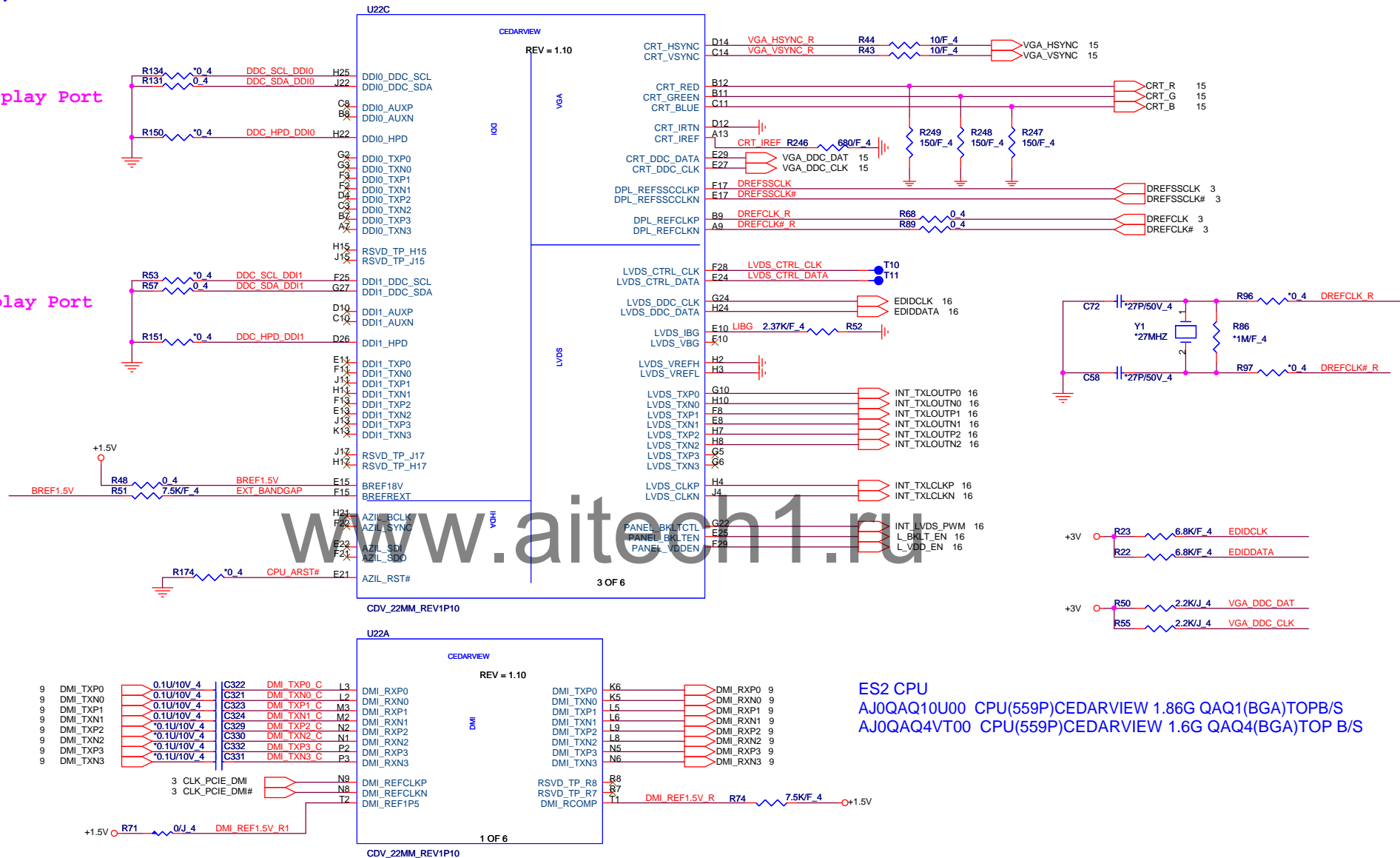




ITP_EN(PIN14)	PIN53/54
0	SRC8#/SRC8
* 1	ITP/ITP#

Disable Display Port

Disable Display Port



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PROJECT : Miata(NM3)

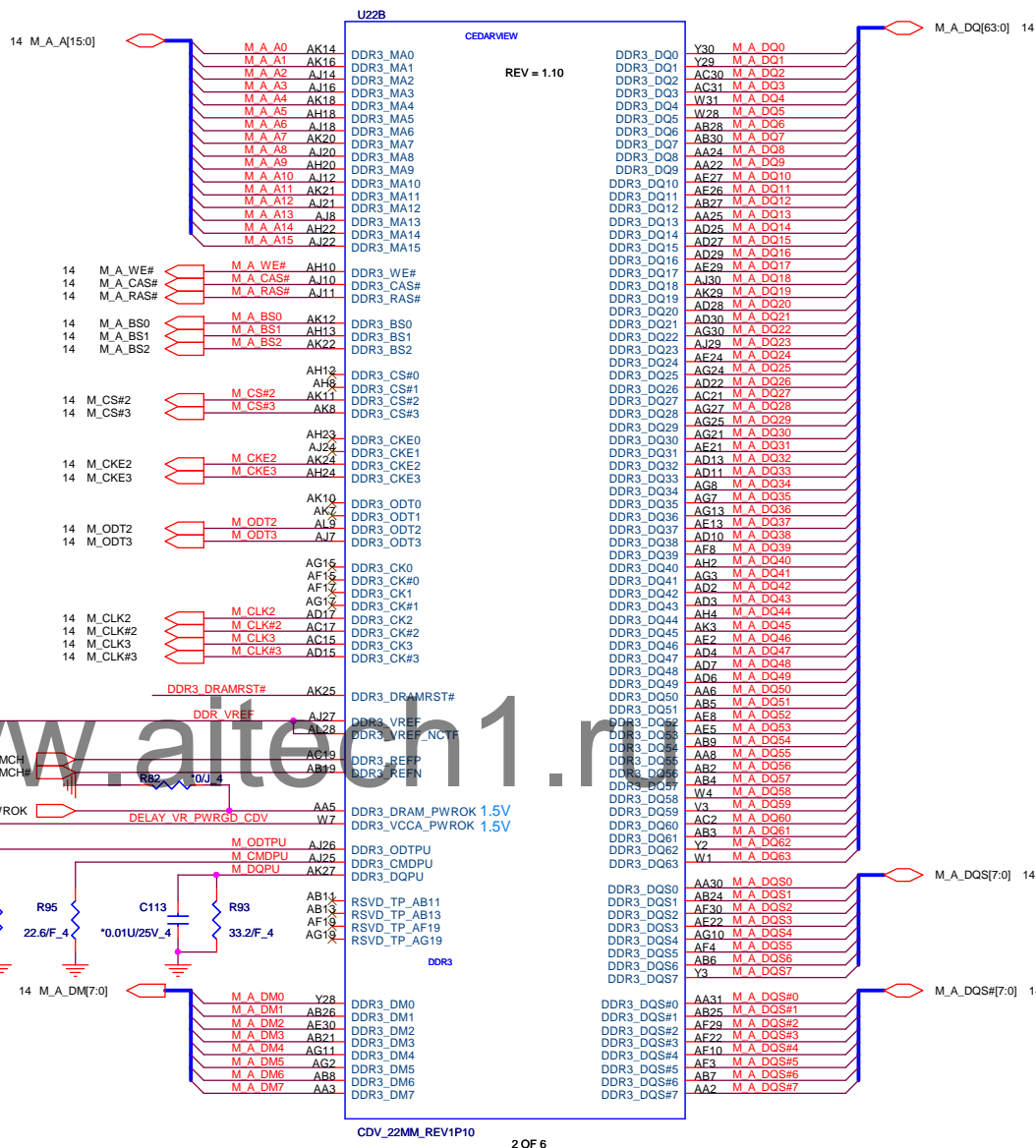
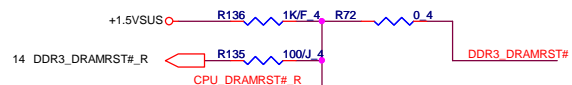
Size	Document Number	Rev
	Cedarview DMI/Display(1/5)	1A
Date:	Thursday, April 07, 2011	Sheet 4 of 32



7,8 VCCDDRAON_1.5

12,25 ECPWROK

DDR3 DRAM RESET

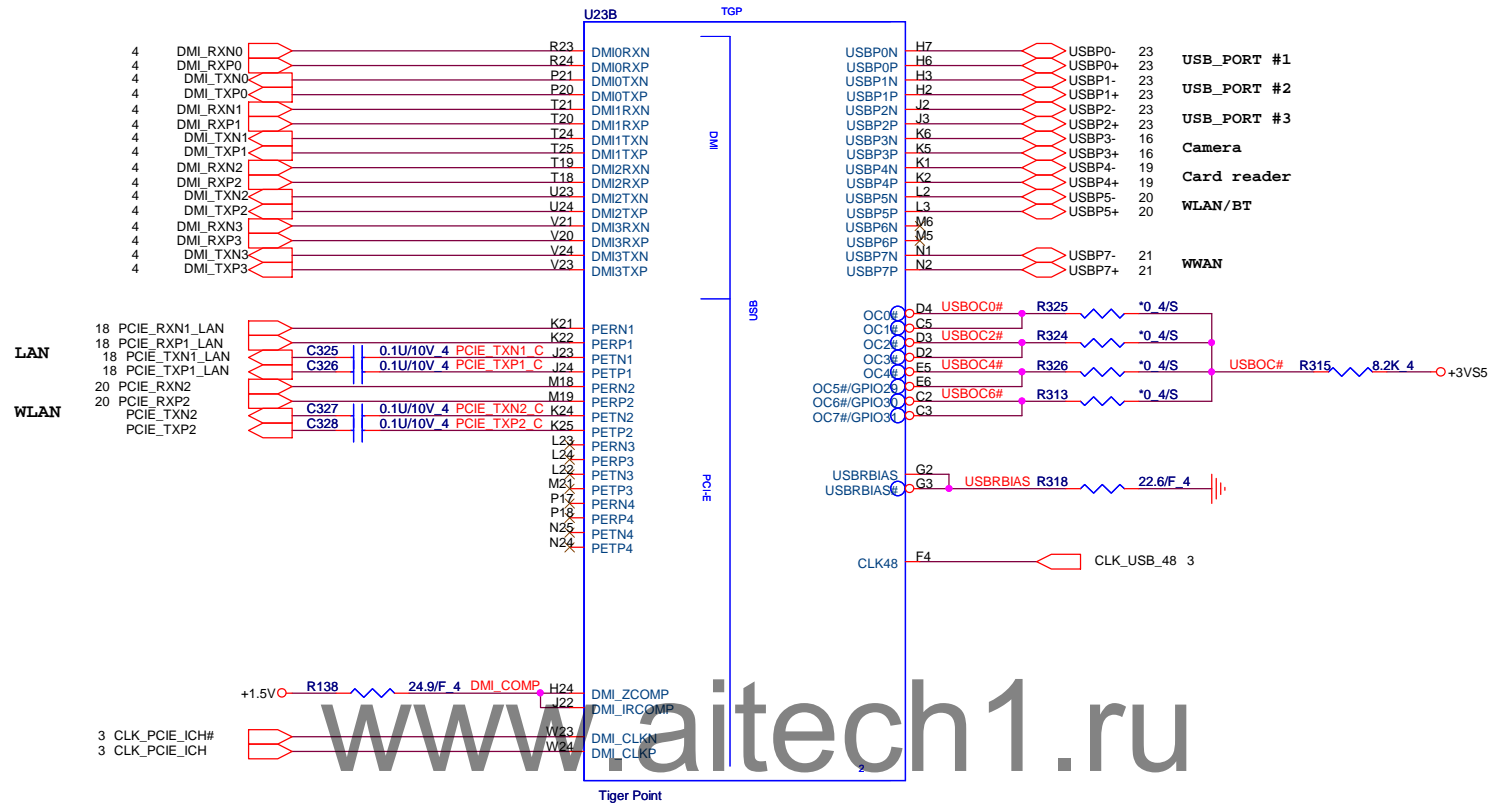


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PROJECT : Miata(NM3)

Size	Document Number	Rev
	Cedarview DDRIII(3/5)	1A
Date:	Thursday, April 07, 2011	Sheet 6 of 32

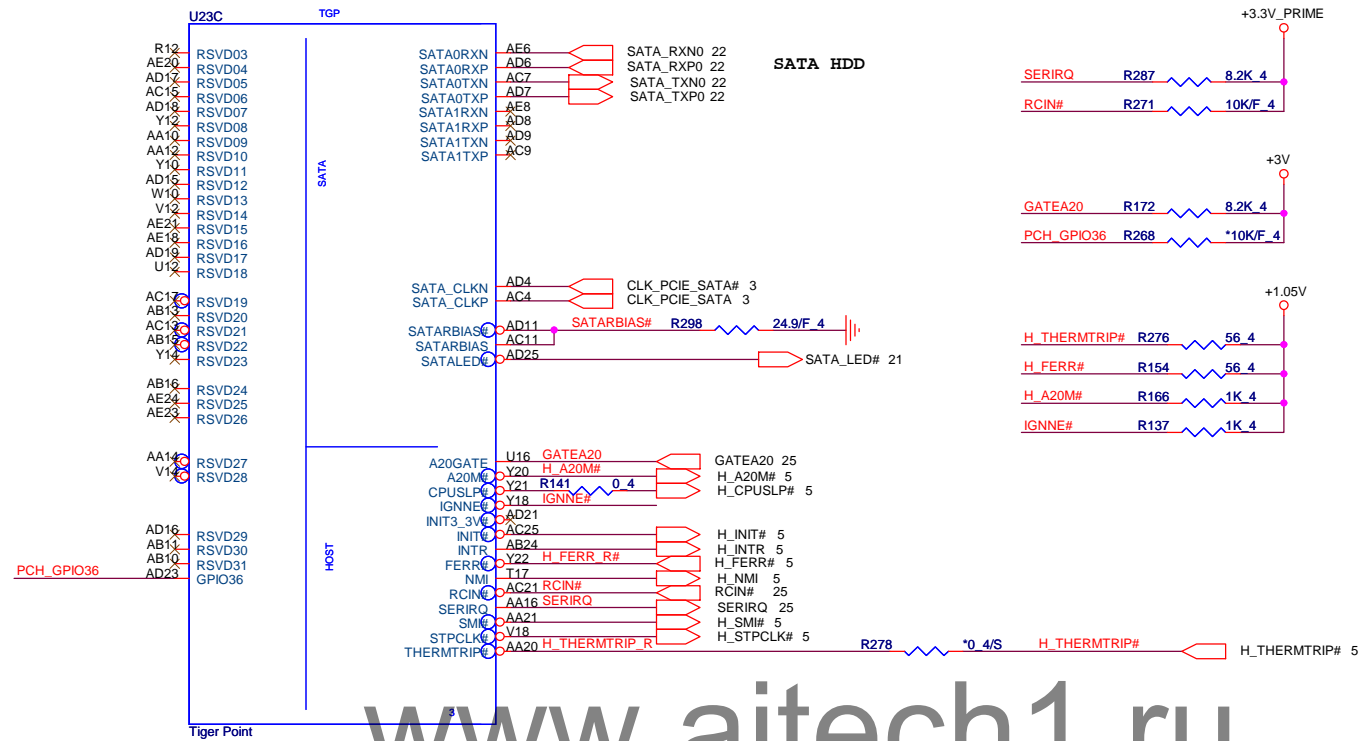




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PROJECT : Miata(NM3)

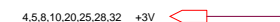
Size	Document Number	Rev
	Tigerpoint DM/PCIE(1/6)	1A
Date:	Thursday, April 07, 2011	Sheet 9 of 32

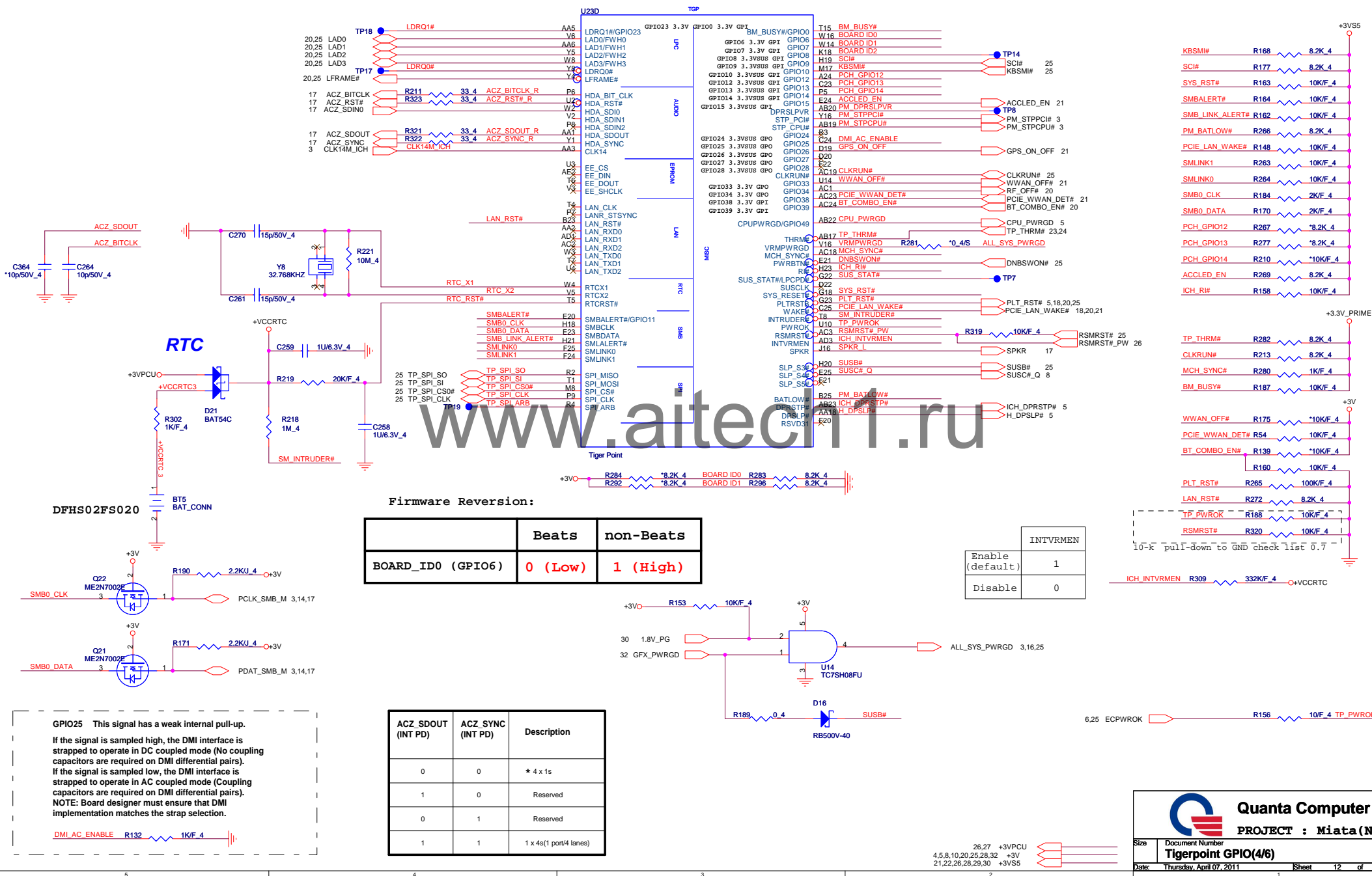


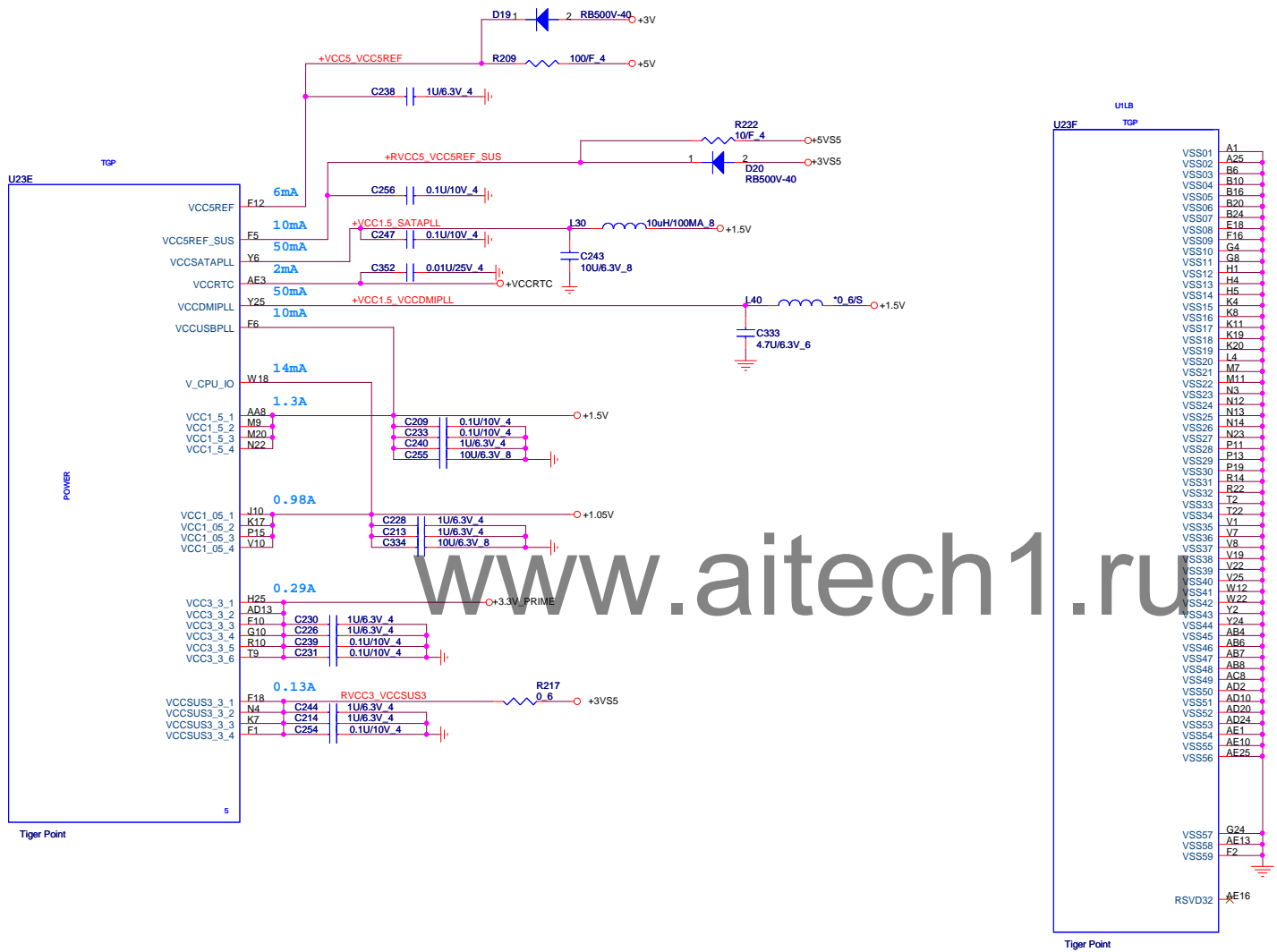
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PROJECT : Miata(NM3)

Size	Document Number	Rev
	Tigerpoint SATA/Host(2/6)	1A
Date:	Thursday, April 07, 2011	Sheet 10 of 32








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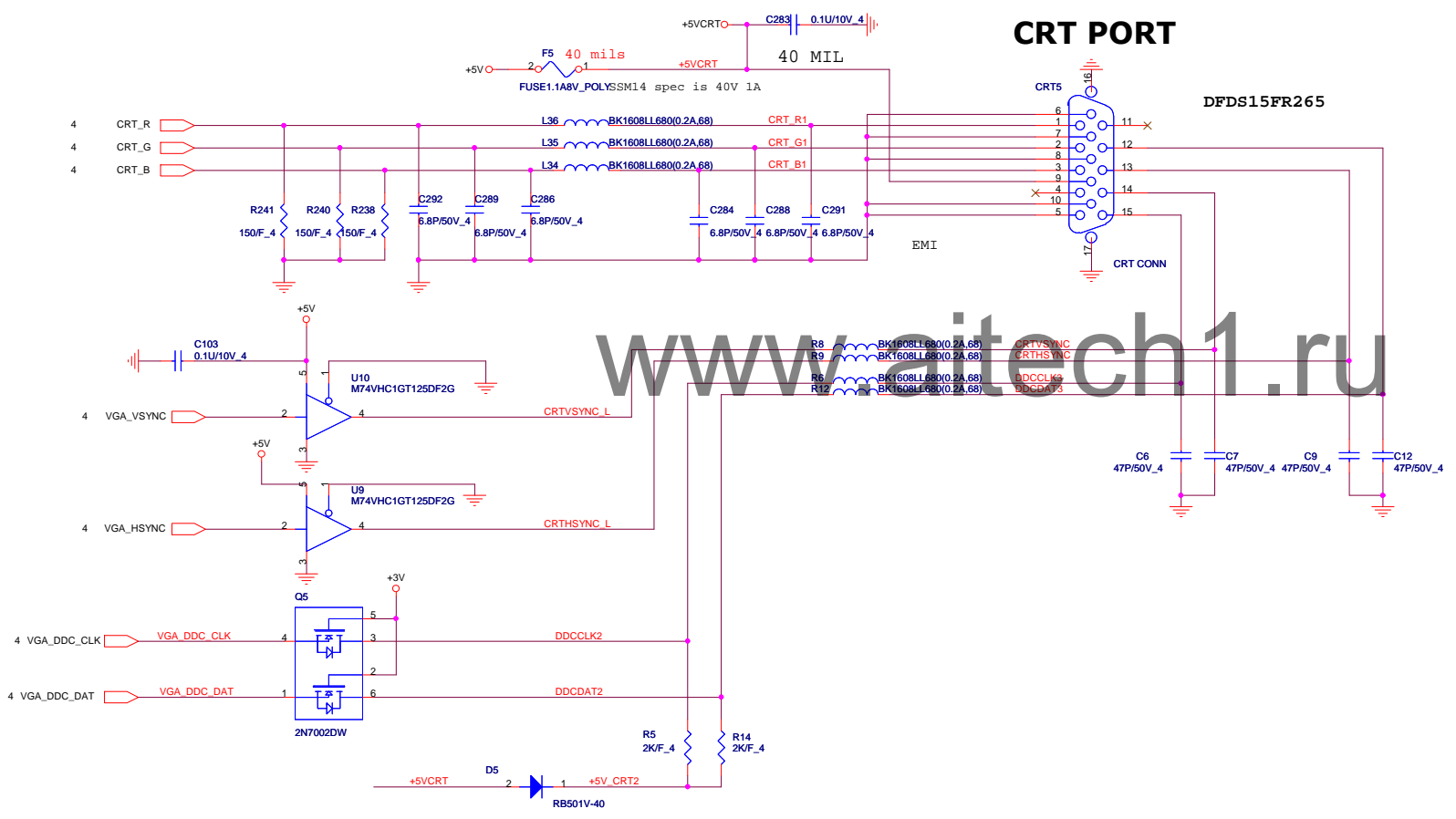


Quanta Computer Inc.
PROJECT : Miata(NM3)

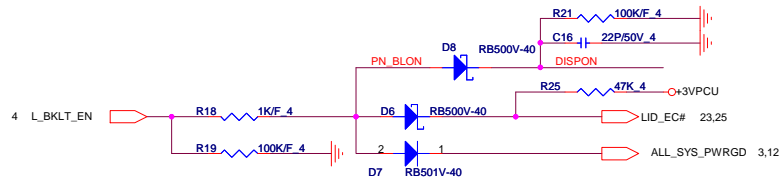
Size	Document Number	Rev
	Tigerpoint Power(5/6)	1A
Date:	Thursday, April 07, 2011	Sheet 13 of 32



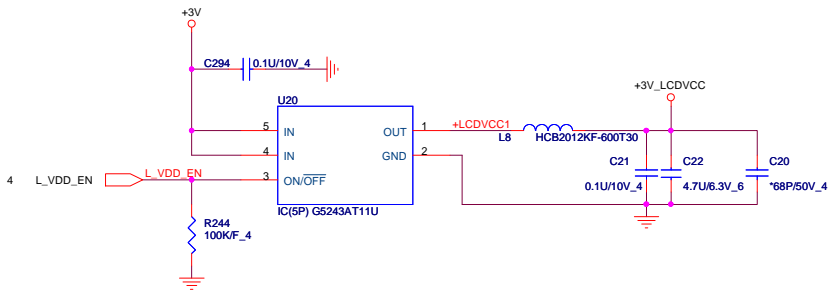
4,5,8,10,20,25,28,32 +3V
5,7,10,22,28,29,32 +1.05V



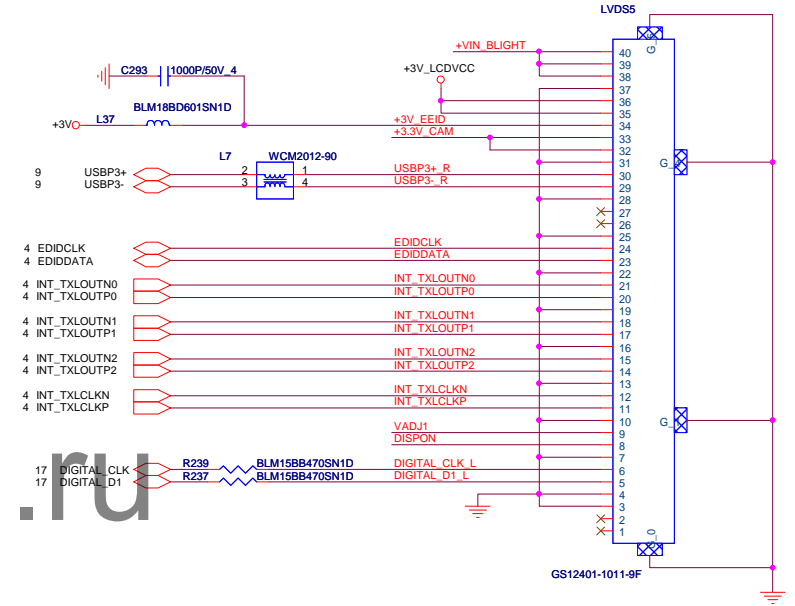
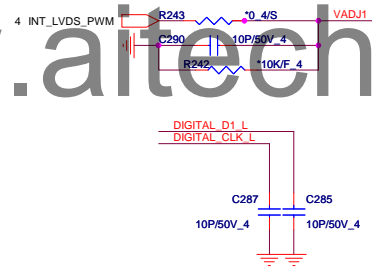
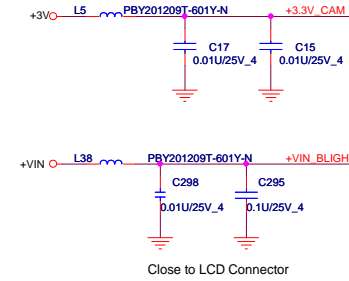
Backlight Control(LDS)



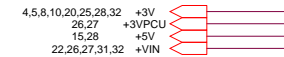
LCD POWER SWITCH

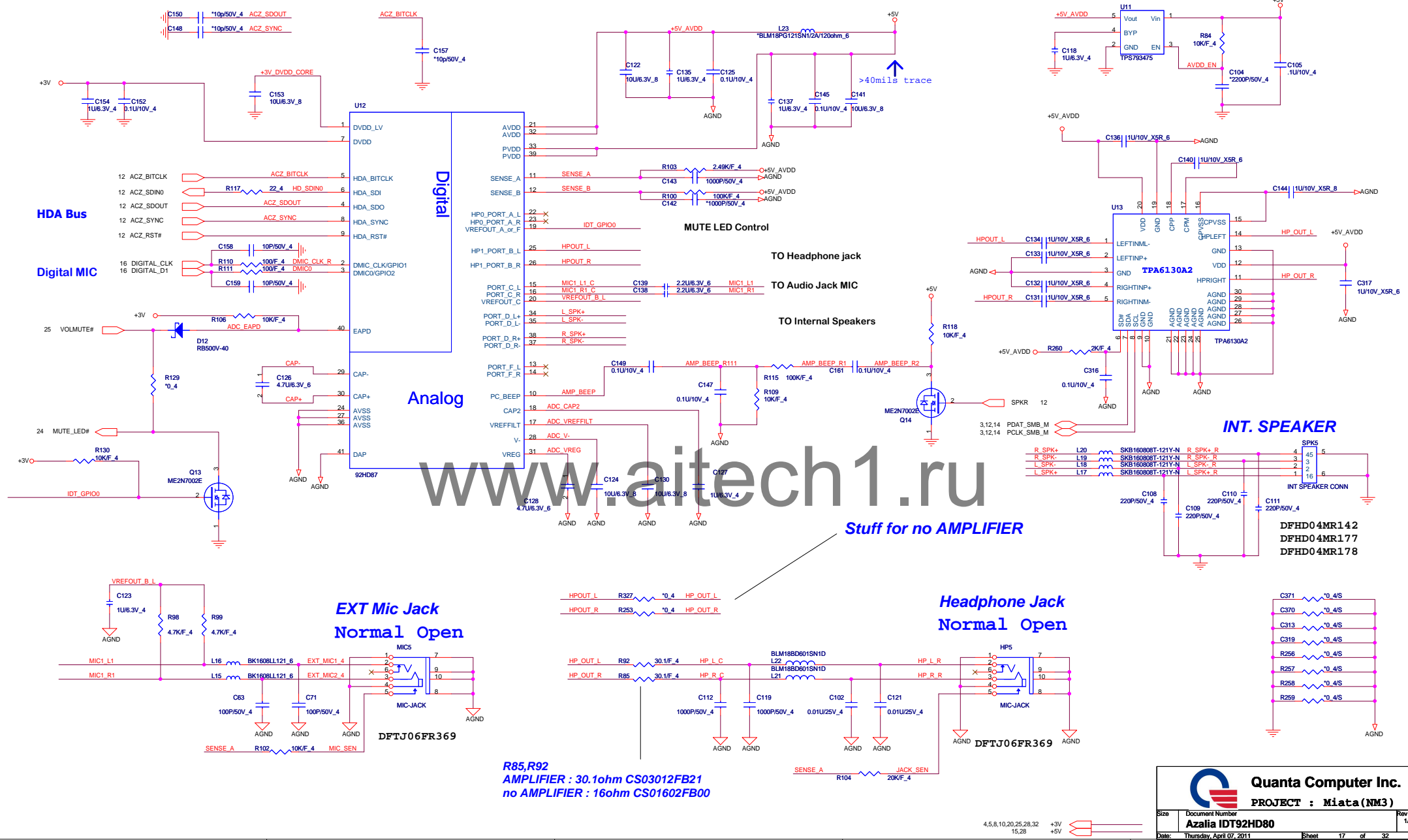


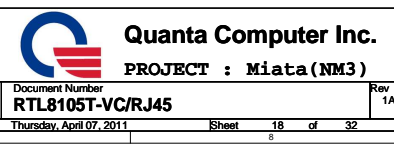
LED Panel(LDS)



DFHS40FS030
DFHS40FS036
DFHS40FS047

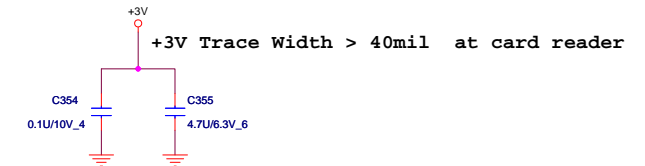
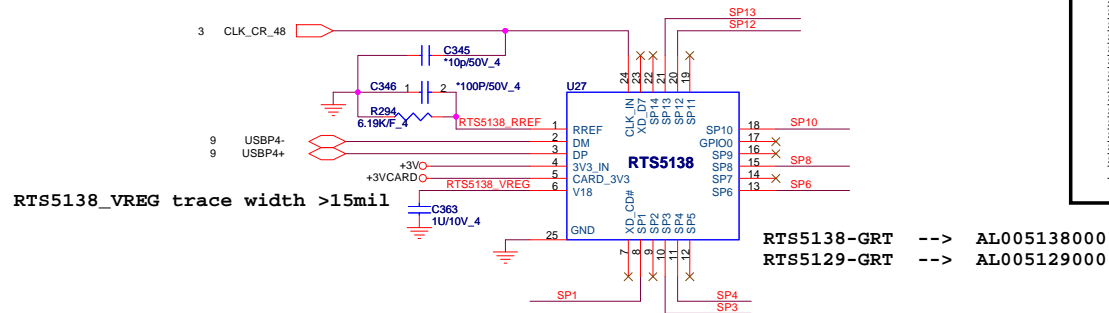






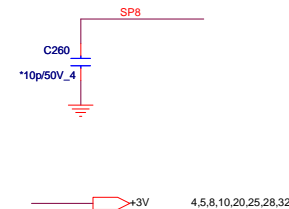
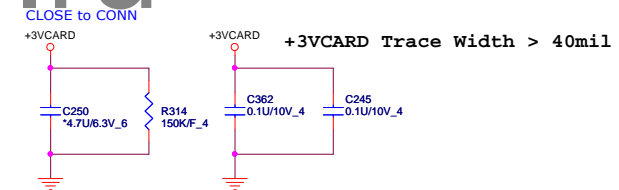
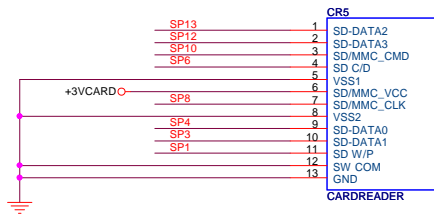
Share Pin

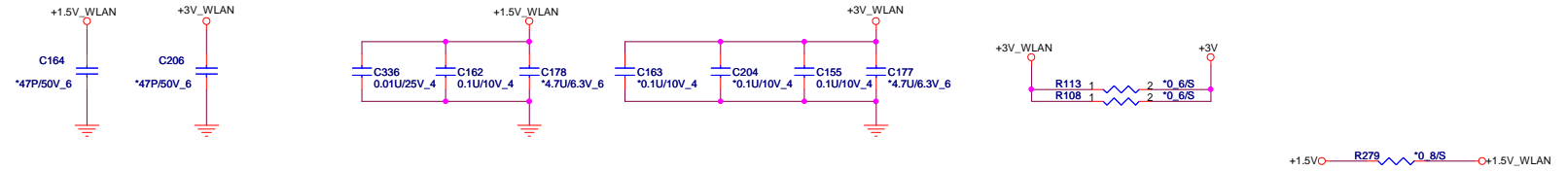
	XD_CD#		
SP1	XD_RDY	SD_WP	MS_CLK
SP2	XD_RE#		MS_INS#
SP3	XD_CE#	SD_D1	
SP4	XD_CLE	SD_D0	MS_D7
SP5	XD_ALE	SD_D7	MS_D3
SP6	XD_WE#	SD_CD#	
SP7	XD_WP	SD_D6	MS_D6
SP8	XD_D0	SD_CLK	MS_D2
SP9	XD_D1	SD_D5	MS_D0
SP10	XD_D2	SD_CMD	
SP11	XD_D3	SD_D4	MS_D4
SP12	XD_D4	SD_D3	MS_D1
SP13	XD_D5	SD_D2	MS_D5
SP14	XD_D6		MS_BS
	XD_D7		



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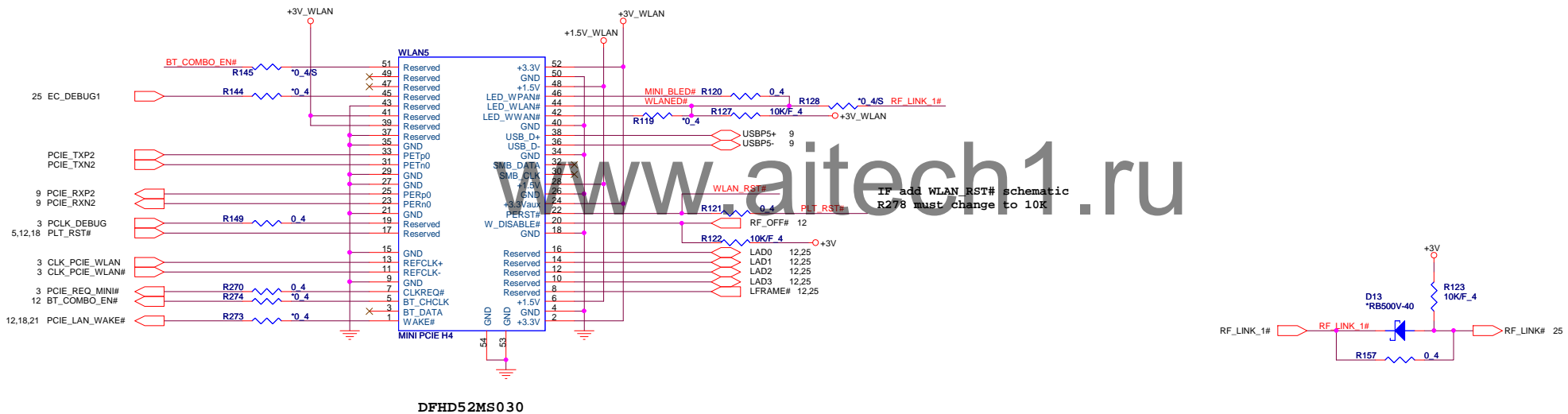
SD / MMC CARD READER



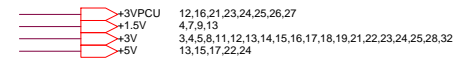


Mini PCI-E Card 1 Half Mini PCI-E WLAN

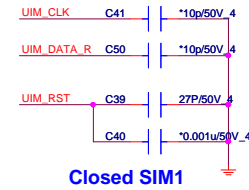
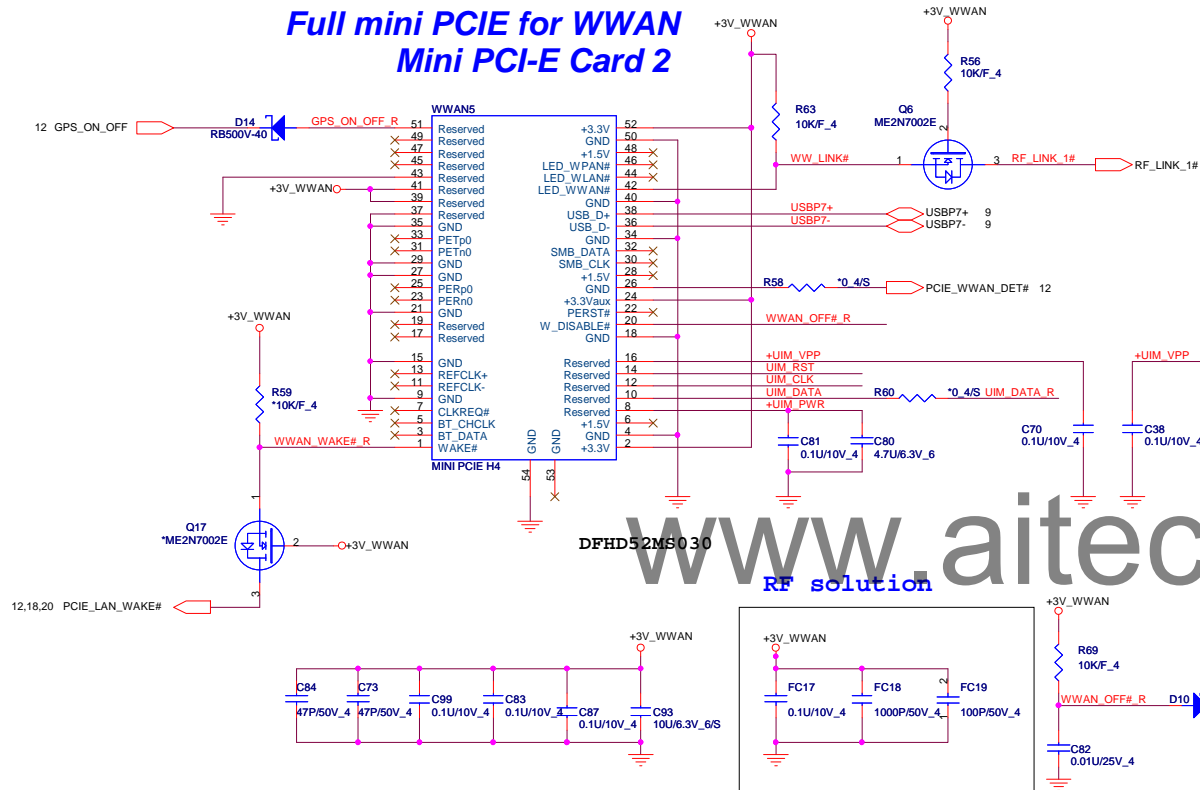
The value of the capacitor is suggest by Siemens HQ expert.
For against 900MHz RF interference. The value of capacitor is 27pF.
For against 1800MHz RF interference. The value of capacitor is 10pF.
1nF/10nF value capacitor use for against ESD purpose.



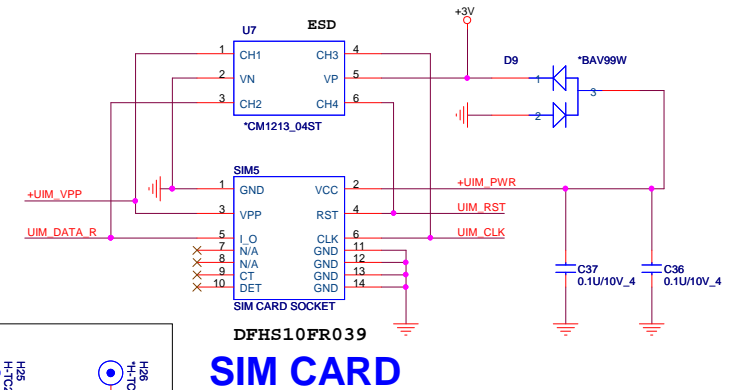
DFHD52MS030



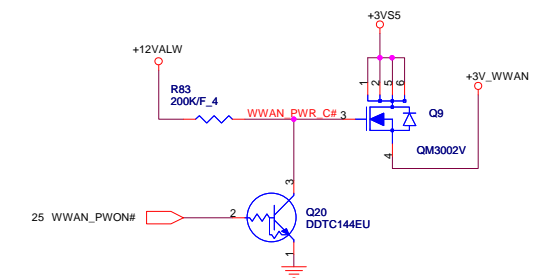
Full mini PCIE for WWAN Mini PCI-E Card 2



SIM CARD SIGNALS ROUTE PARALLEL



SIM CARD



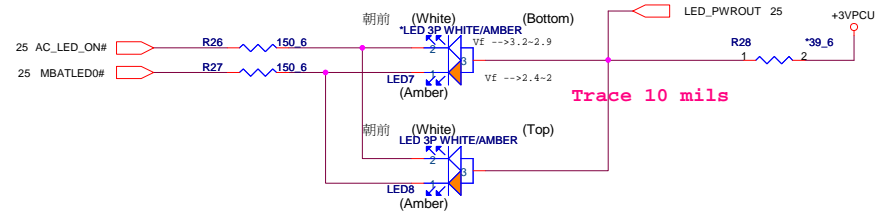
DFHD52MS030

RF solution

SATA/G sensor LED

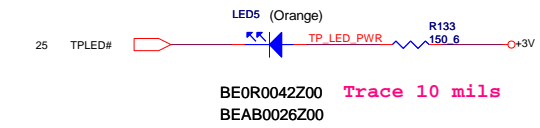
Touchpad LED

Charging & Discharging/LED



G sensor
White+Yellow
BEWH0007ZA0
BEWH0009ZA0

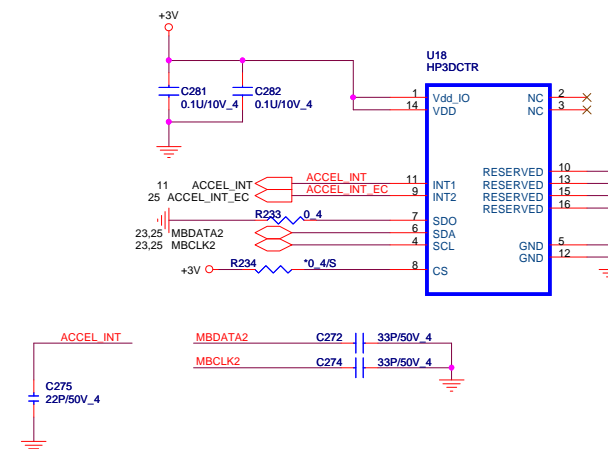
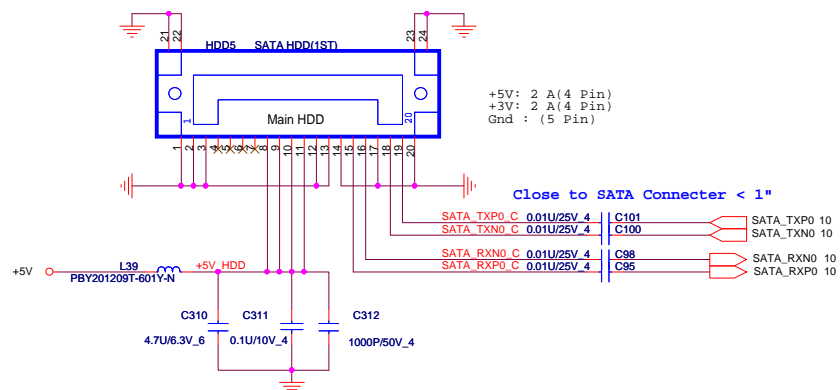
non- G sensor
White
BEWH0046Z00
BEWH0051Z00



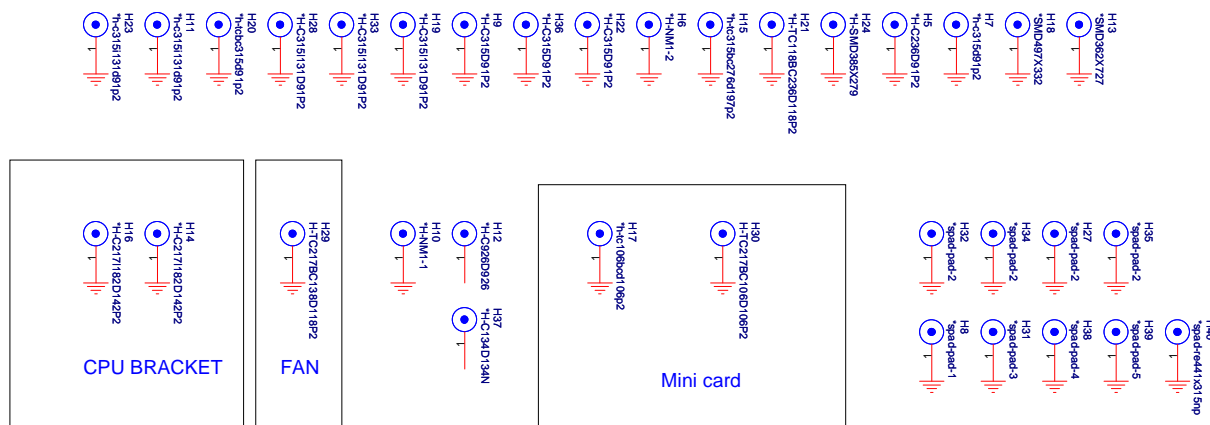
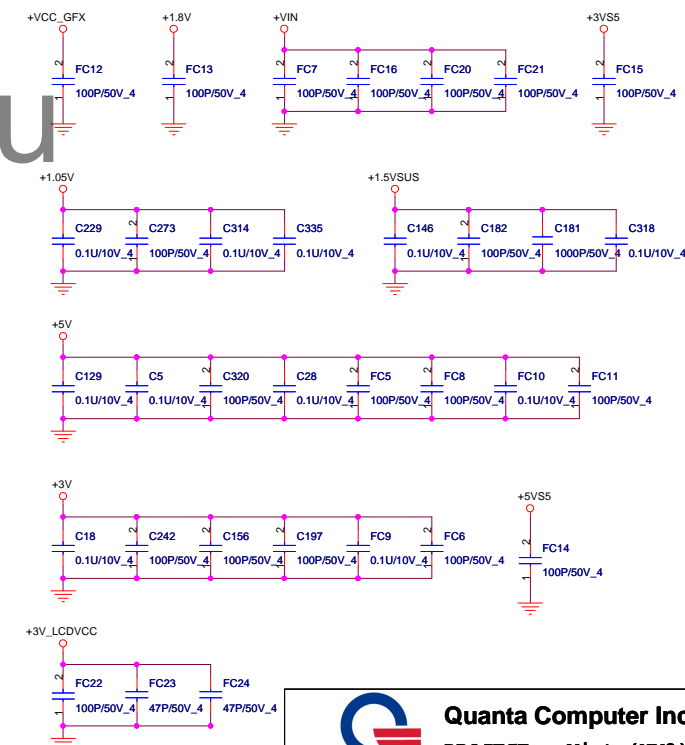
BEOR0042Z00
BEAB0026Z00

DFHD20MR023

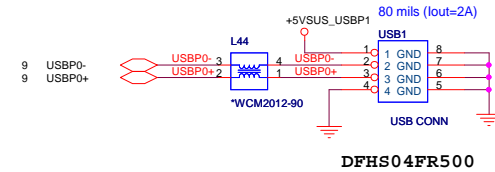
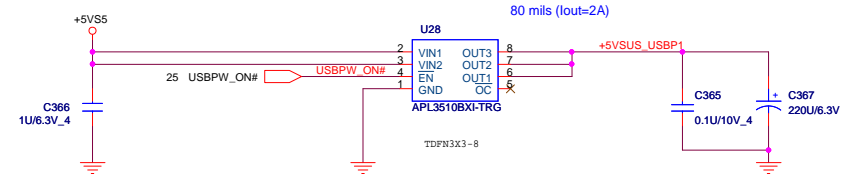
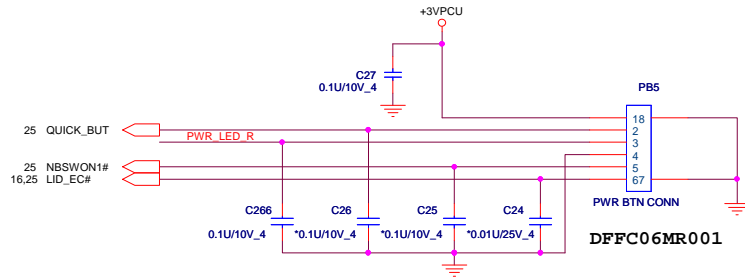
DC Current rating: 0.5 A



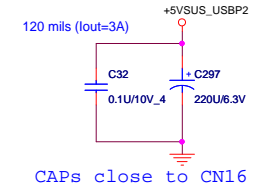
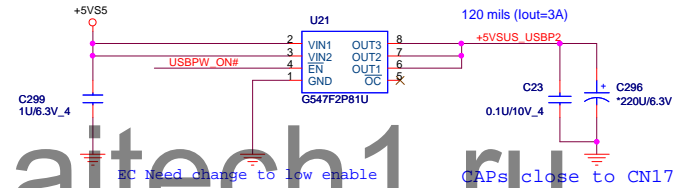
RF solution



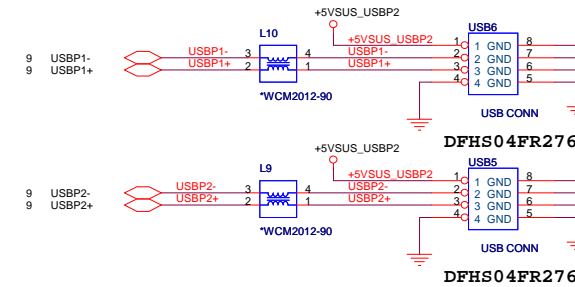
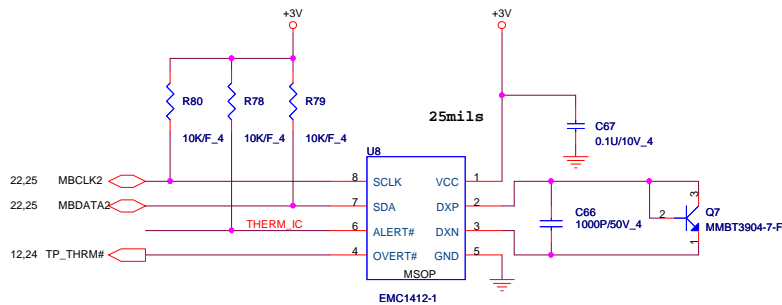
1x Left side USB port supports Keyed USB.



For Right 2xUSB Ports PWR



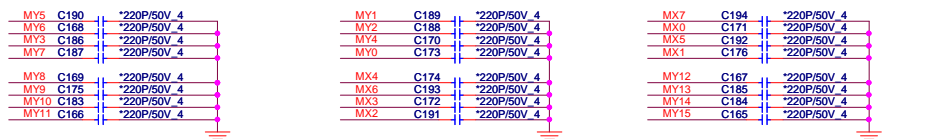
CPU Thermal



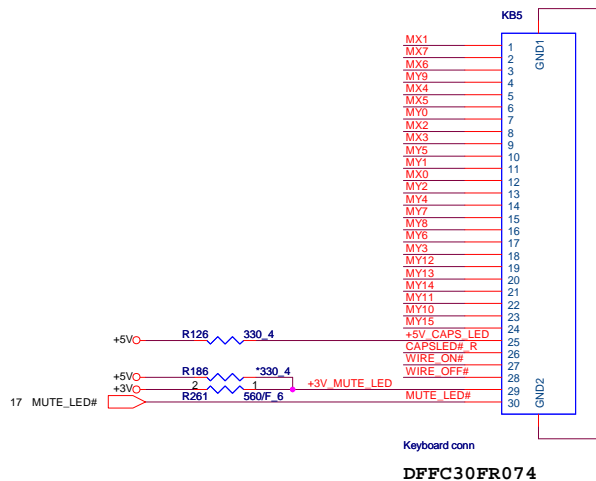
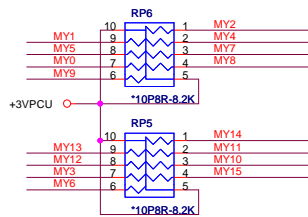
+3VPCU 26,27

+3V 4,5,8,10,20,25,28,32

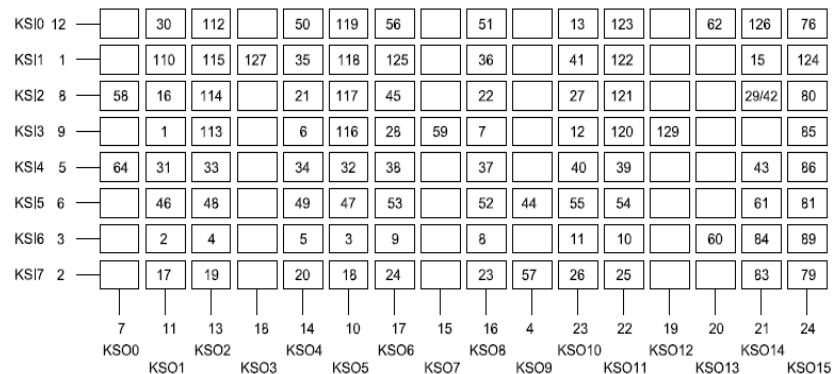
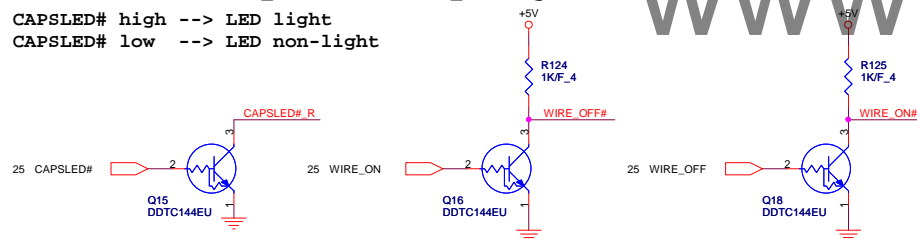
Keyboard (KBC)



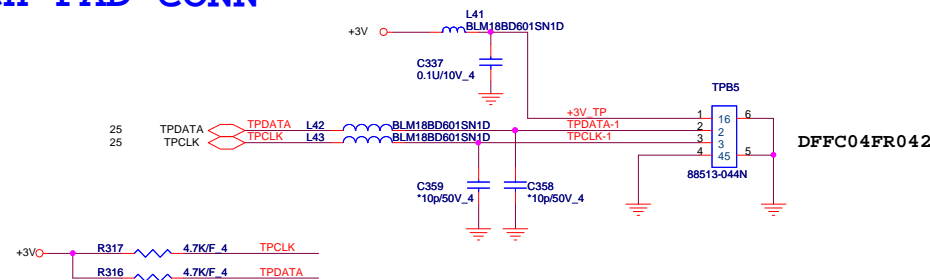
KEYBOARD PULL-UP



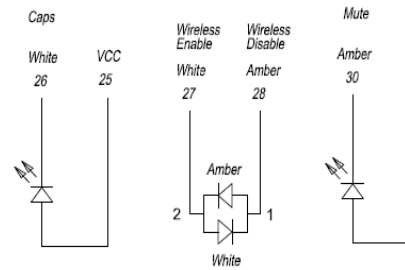
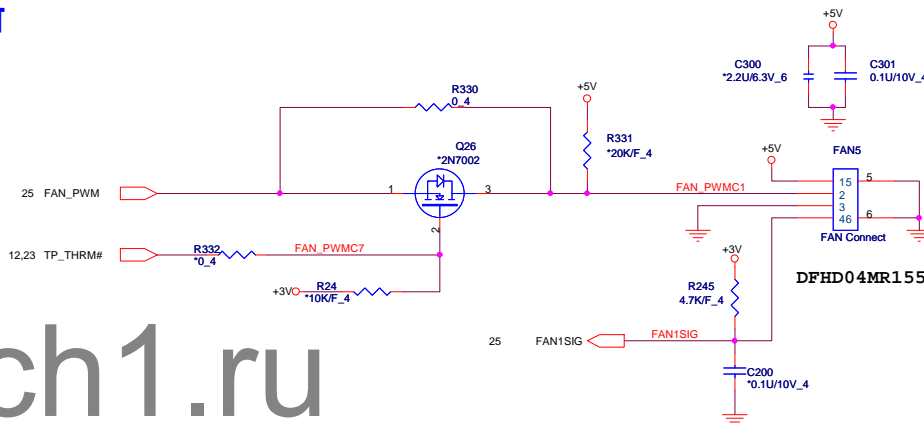
Enable WLAN --> wire_on high and wire_off low
Disable WLAN --> wire_on low and wire_off high
CAPSLED# high --> LED light
CAPSLED# low --> LED non-light



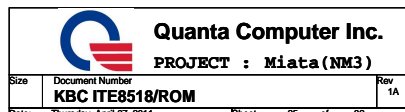
TOUCH PAD CONN

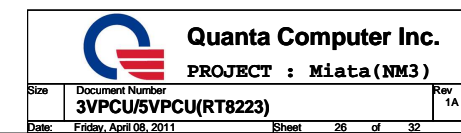


CPU FAN

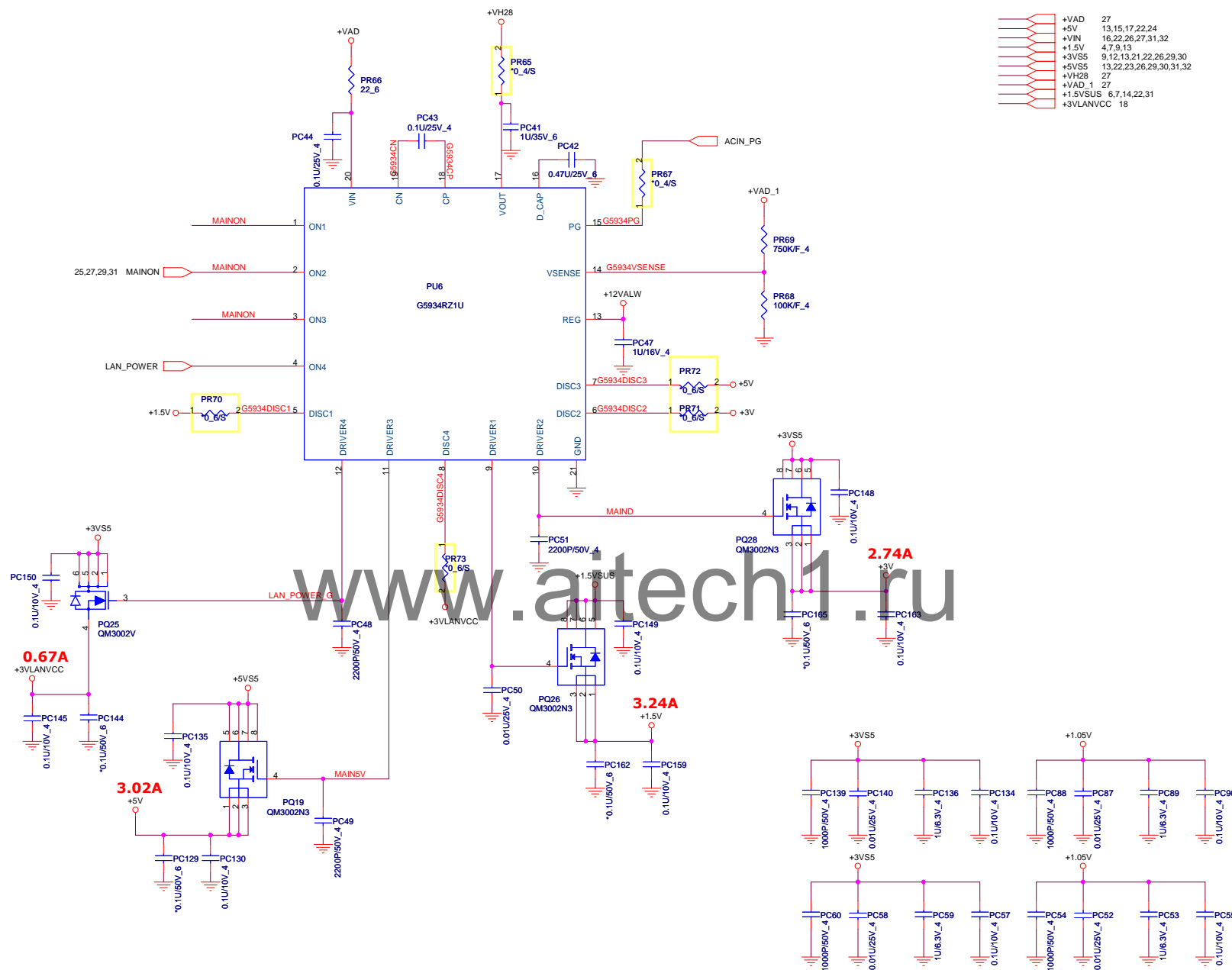


26,27 +3VPCU
15,28 +5V
4,5,8,10,20,25,28,32 +3V

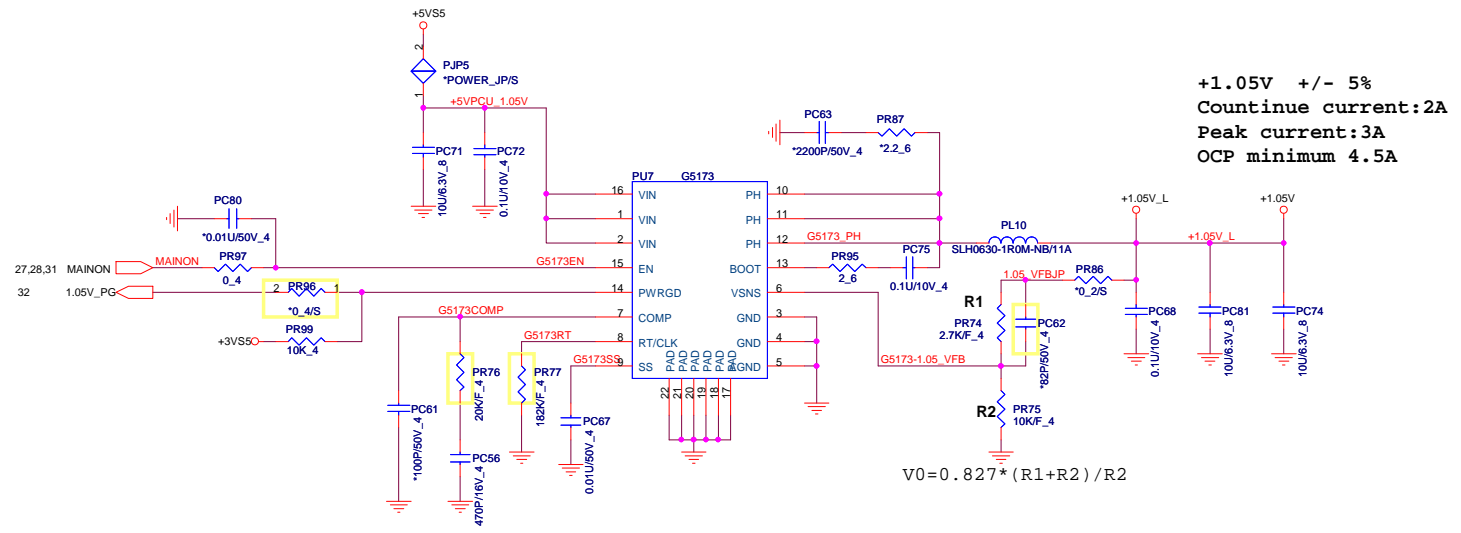




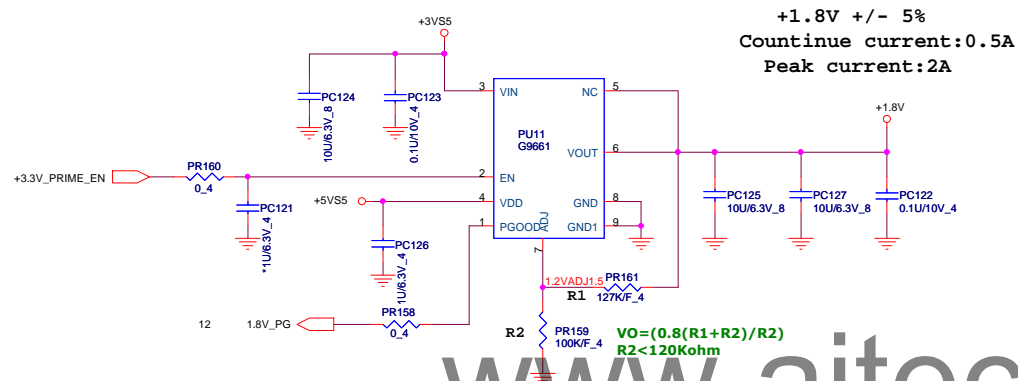




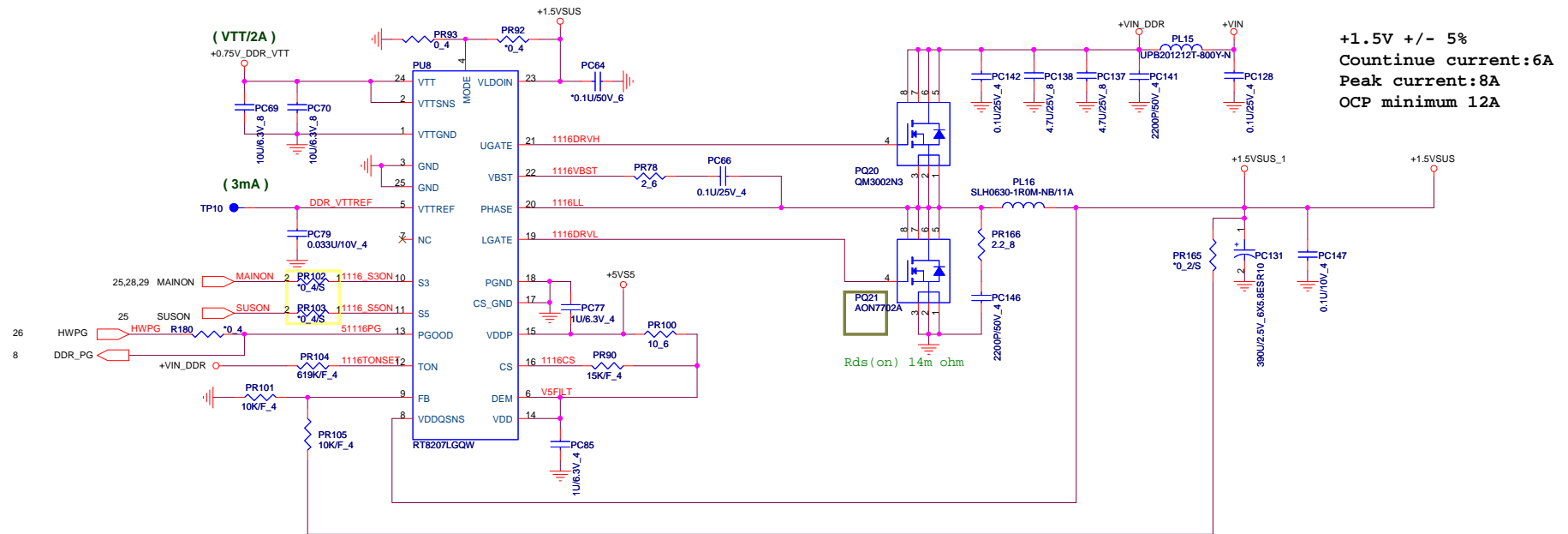
VCCP1.05V



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PROJECT : Miata(NM3)

Size Document Number
DDR2 +1.5VSUS/+0.75V

Date: Friday, April 08, 2011 Sheet 31 of 32 Rev 1A

+GFXORE Volt +/- 5%
Countinue current:1.5A
Peak current:2A
OCP minimum:3A

+CPUVORE Volt +/- 5%
Countinue current:5A
Peak current:6.1A
OCP minimum:9A

